GOA UNIVERSITY
Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - II EXAMINATION
HELD IN MAY 2019
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3201 P R No : 201309124 Sex : M Name : DHURI VIBHAV LAXMANNATH
No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
ASIC Design & FPGA					
	Theory	4	BB	P	
	IA	2	AB	P	
Digital Signal Processors & Embedded Systems					
	Theory	4	BB	P	
	IA	2	BC	P	
Design for Testability & E-Waste Management					
	Theory	4	BB	P	
	IA	2	AA	P	
Processor Architecture & Parallel Processing					
	Theory	4	BB	P	
	IA	2	BB	P	
Memory Design					
	Theory	4	BB	P	
	IA	2	BC	P	
Parallel Processing Lab					
	IA	2	BB	P	
	Practical	2	BB	P	
FPGA & Embedded Systems Lab					
	IA	2	AB	P	
	Practical	2	BB	P	
	Total :	38			7.11 P PASSES

Seat No : 3202 P R No : 201401159 Sex : M Name : KSHIRSAGAR MADHAV ALIAS PRASAD
No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
ASIC Design & FPGA					
	Theory	4	BC	P	
	IA	2	AB	P	
Digital Signal Processors & Embedded Systems					
	Theory	4	BB	P	
	IA	2	CC	P	
Design for Testability & E-Waste Management					
	Theory	4	BB	P	
	IA	2	AO	P	
Processor Architecture & Parallel Processing					
	Theory	4	AB	P	
	IA	2	AB	P	
Memory Design					
	Theory	4	CC	P	
	IA	2	AB	P	
Parallel Processing Lab					
	IA	2	AB	P	
	Practical	2	AB	P	
FPGA & Embedded Systems Lab					
	IA	2	AB	P	
	Practical	2	AA	P	
	Total :	38			7.26 P PASSES

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P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

Discrepancy, if any in the Result shall be informed to the University within a week from the date of receipt of the result by the college

GOA UNIVERSITY
Taleigao Plateau, Goa 403 206

RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - II EXAMINATION HELD IN MAY 2019
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3203 P R No : 201408325 Sex : F Name : LOBO JOELLA
No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
ASIC Design & FPGA					
	Theory	4	BB	P	
	IA	2	AA	P	
Digital Signal Processors & Embedded Systems					
	Theory	4	BB	P	
	IA	2	AA	P	
Design for Testability & E-Waste Management					
	Theory	4	BB	P	
	IA	2	AA	P	
Processor Architecture & Parallel Processing					
	Theory	4	AB	P	
	IA	2	AB	P	
Memory Design					
	Theory	4	AB	P	
	IA	2	AA	P	
Parallel Processing Lab					
	IA	2	AB	P	
	Practical	2	AB	P	
FPGA & Embedded Systems Lab					
	IA	2	AB	P	
	Practical	2	AB	P	
	Total :	38			7.89 P
					PASSES

Seat No : 3204 P R No : 201406699 Sex : F Name : NAIK AKSHATA UDAY
No Of Attempts : 1

		No Of Credits	Grade Obtained		SGPA
ASIC Design & FPGA					
	Theory	4	AA	P	
	IA	2	AA	P	
Digital Signal Processors & Embedded Systems					
	Theory	4	AB	P	
	IA	2	AA	P	
Design for Testability & E-Waste Management					
	Theory	4	BB	P	
	IA	2	BB	P	
Processor Architecture & Parallel Processing					
	Theory	4	AA	P	
	IA	2	AA	P	
Memory Design					
	Theory	4	BB	P	
	IA	2	AA	P	
Parallel Processing Lab					
	IA	2	AB	P	
	Practical	2	AB	P	
FPGA & Embedded Systems Lab					
	IA	2	BC	P	
	Practical	2	BC	P	
	Total :	38			7.95 P
					PASSES

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average

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GOA UNIVERSITY
Taleigao Plateau, Goa 403 206

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RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - II EXAMINATION HELD IN MAY 2019

Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3205 P R No : 201308747 Sex : M Name : TENGSE PRANAV MANJANATH

No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
ASIC Design & FPGA				
	Theory	4	BC P	
	IA	2	AA P	
Digital Signal Processors & Embedded Systems				
	Theory	4	BB P	
	IA	2	BC P	
Design for Testability & E-Waste Management				
	Theory	4	CC P	
	IA	2	AB P	
Processor Architecture & Parallel Processing				
	Theory	4	BB P	
	IA	2	AA P	
Memory Design				
	Theory	4	BC P	
	IA	2	AA P	
Parallel Processing Lab				
	IA	2	AB P	
	Practical	2	AA P	
FPGA & Embedded Systems Lab				
	IA	2	AA P	
	Practical	2	BB P	
	Total :	38		7.16 P PASSES

Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By :

Checked By :

Date :

10-9-19

Aniketh Gaonkar
Assistant Registrar-E(Proff.)

Prof. Anuradha Wagle
Controller Of Examinations

Prof. Y. V. Reddy
Registrar

GOA UNIVERSITY
TALEIGAO PLATEAU GOA

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